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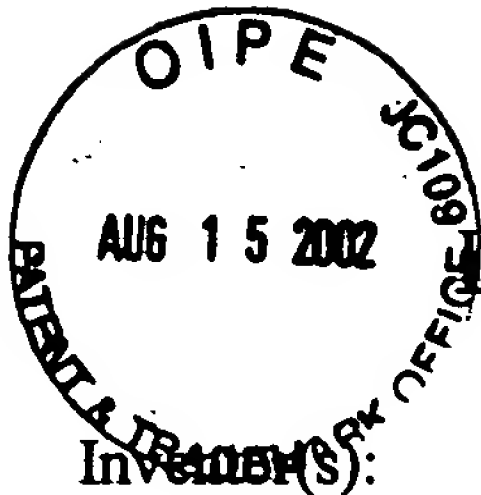
PATENT

2183

#7

T.P.

08/23/02



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor(s): Marc Tremblay

Assignee: Sun Microsystems, Inc.

Title: GROUPING LOGIC CIRCUIT IN A PIPELINED SUPERSCALAR
PROCESSOR

Application No.: 09/583,097

Filed: August 2, 1999

Examiner: E. Chan

Group Art Unit: 2183

Atty. Docket No.: 004-1391-1 (formerly
SP-1391-2C US)

Conf. No.: 7166

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**POWER OF ATTORNEY BY ASSIGNEE OF ENTIRE INTEREST
(INCLUDING REVOCATION OF PRIOR POWERS)**

Dear Sir:

Assignee of record of the entire interest of the above-identified application hereby
revokes all powers of attorney previously given and appoints the following attorney(s) and/or
agent(s) to prosecute this application and to transact all business in the United States Patent and
Trademark Office connected therewith:

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PATENT TRADEMARK OFFICE

STATEMENT UNDER 37 CFR § 3.73(b) ESTABLISHING RIGHT OF ASSIGNEE TO TAKE ACTION

The assignee, Sun Microsystems, Inc., seeks to take action in the above-identified patent application. An assignment of the above-identified patent application from the inventors to the assignee is recorded in the Patent and Trademark Office at Reel 8052/Frame 0437.

The undersigned, whose title is given below, is authorized to act on behalf of the assignee.

August 1, 2002
Date

Kenneth Olsen, 26,493
Vice President, Intellectual Property
Sun Microsystems, Inc.

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